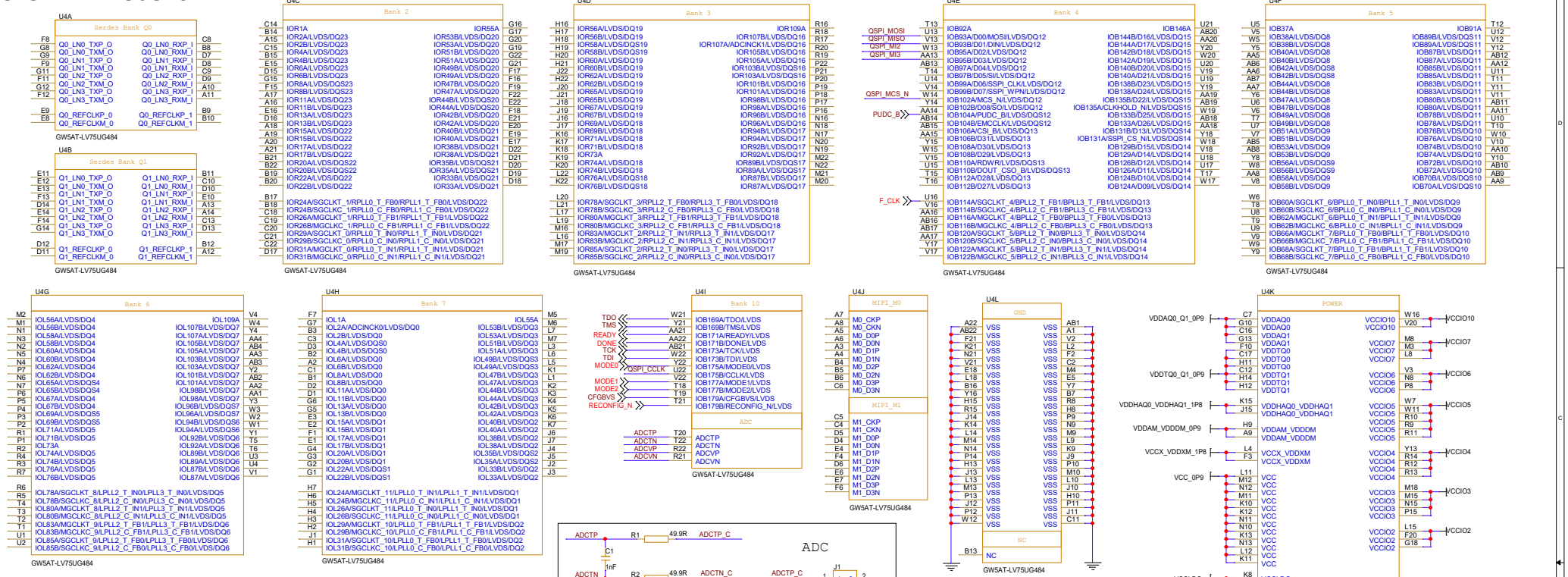
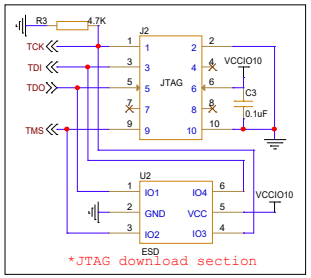
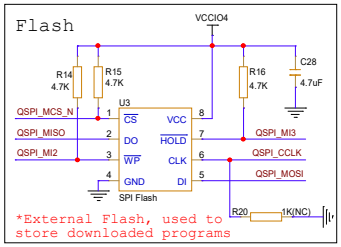
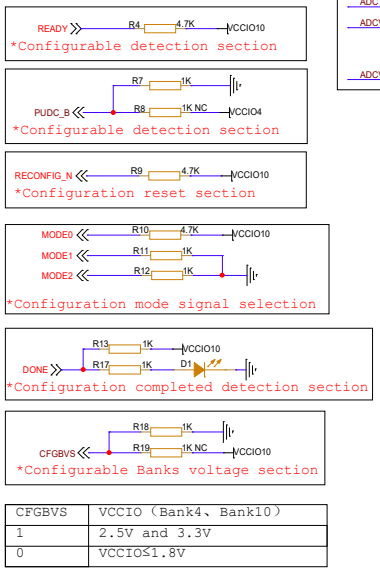
GW5AT-LV75UG484

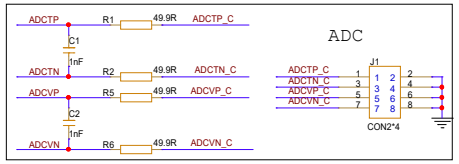


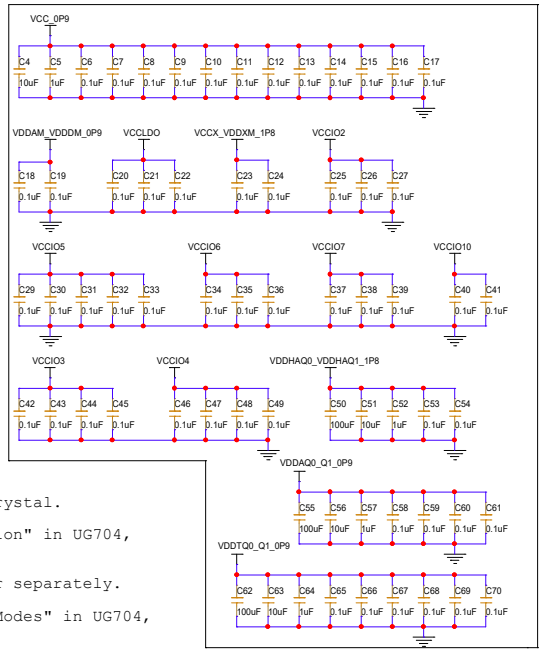






CFGBVS	VCCIO (Bank4、Bank10)
1	2.5V and 3.3V
0	VCCIO≤1.8V





Name	Description	Min.	Max.
FPGA Logic			
V_{CC}	Core voltage	0.87V	1.03V
V_{CCIO}	I/O Bank voltage	1V	3.465V
V_{CCX}	Auxiliary voltage	1.71V	1.89V
V_{CCLDO}[1]	Power supply voltage for the internal LDO module that powers the PLL and SRAM	1.14V	1.89V
Gigabit Transceiver			
V_{DDHAQ*}	Analog high power supply voltage	1.71V	1.89V
V_{DDAQ*}	Analog core power supply voltage	0.87V	1.03V
V_{DDTQ*}	TX power supply voltage	0.87V	1.03V
V_{DDDQ*}	Digital core power supply voltage	0.87V	1.03V
MIPI			
V_{DDAM}	Analog core power supply voltage	0.87V	1.03V
V_{DDXM}	Analog auxiliary voltage power supply voltage	1.71V	1.89V
V_{DDDM}	Digital core power supply voltage	0.87V	1.03V

Note!

[1] The greater the V_{CCLDO} voltage, the higher the power consumption.

If multiple power supplies are shorted on some packages or PCBs, it is necessary to take the intersection of the ranges of all shorted power supplies to satisfy the needs of multiple power supplies at the same time.

Notes:
1.F_CLK signal is an external input clock signal.
  It is recommended that F_CLK signal be provided through an active oscillator crystal.
2.External Flash memory is used to store downloaded programs.
  For details about SPI Flash model selection, see "Chapter 4.5 SPI Flash Selection" in UG704, Arora V FPGA Products Programming and Configuration Guide.
3.It is recommended that add an ESD protection chip to the JTAG download circuit.
4.VCC core voltage requires a large current, so it is recommended to supply power separately.
5.The MODE pin is the GowinCONFIG configuration mode selection signal.
For details about how to select the Mode signal, see "Chapter 3.1 Configuration Modes" in UG704, Arora V FPGA Products Programming and Configuration Guide.

Title	GOWIN Minimum System Diagram		
Size C	Document Number GW5AT-LV75UG484		Rev 1.5
Date: Wednesday, October 14, 2026	Sheet 1 of 1		